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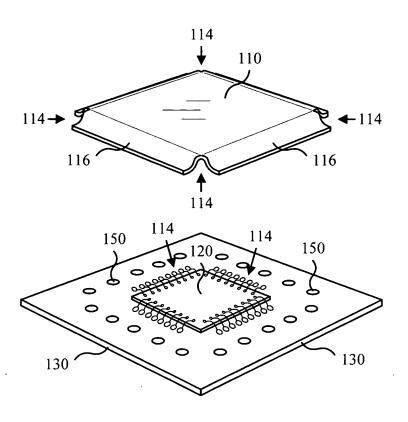


Fig. 1



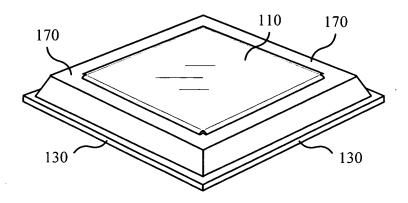
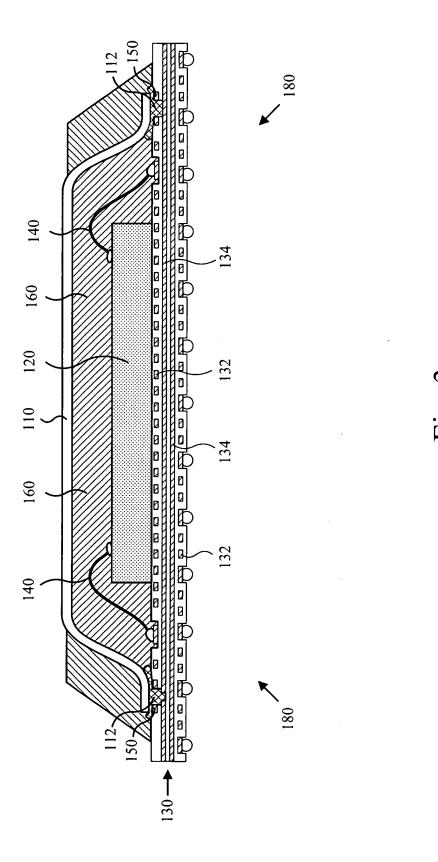


Fig. 2



400

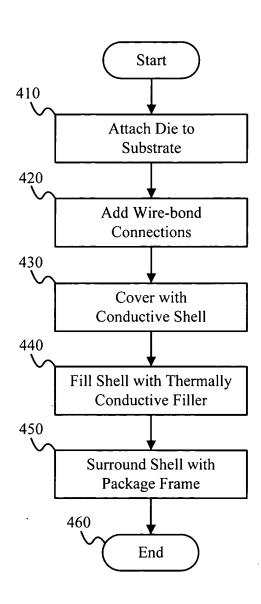


Fig. 4

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110a

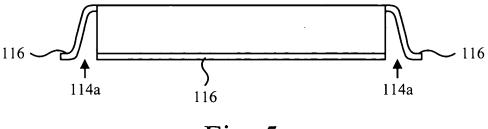


Fig. 5

110a

114a

114a

116

1114a

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1114a

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Fig. 6

Title: EMI Shielded Integrated Circuit Packaging Apparatus Method and System
Inventors: Awujoola et al.
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110b

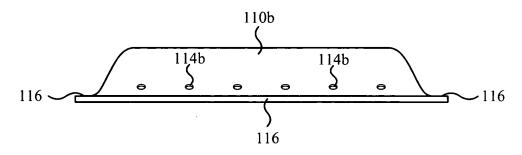


Fig. 7

110b

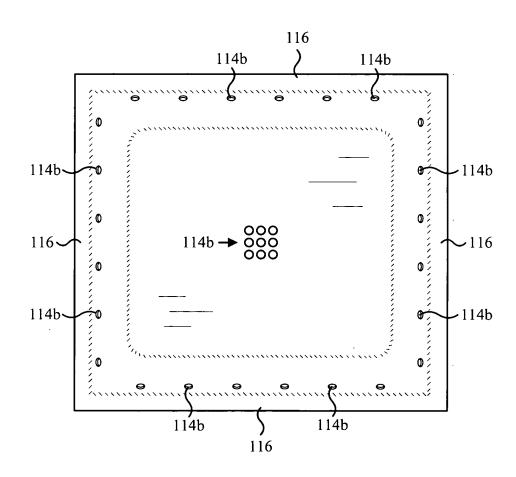


Fig. 8

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110c

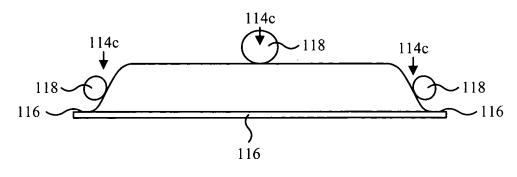


Fig. 9

110c

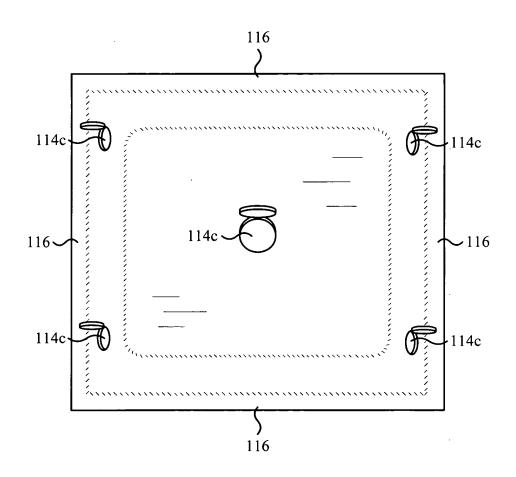


Fig. 10